# 2SB1319

## Silicon PNP epitaxial planar type

### For low-frequency power amplification

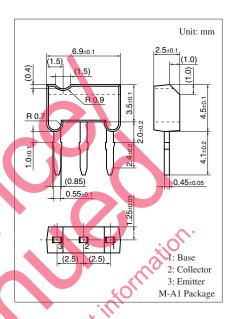
#### ■ Features

- Low collector-emitter saturation voltage V<sub>CE(sat)</sub>
- Large collector current I<sub>C</sub>
- M type package allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	$V_{CBO}$	-30	V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-20	V
Emitter-base voltage (Collector open)	$V_{EBO}$	-7	V
Collector current	$I_C$	-5	A
Peak collector current	$I_{CP}$	-10	A
Collector power dissipation *	P <sub>C</sub>	1	W
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Note) \*: Print circuit board: Copper foil area of 1 cm<sup>2</sup> or more, and the board thickness of 1.7 mm for the collector portion



# ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

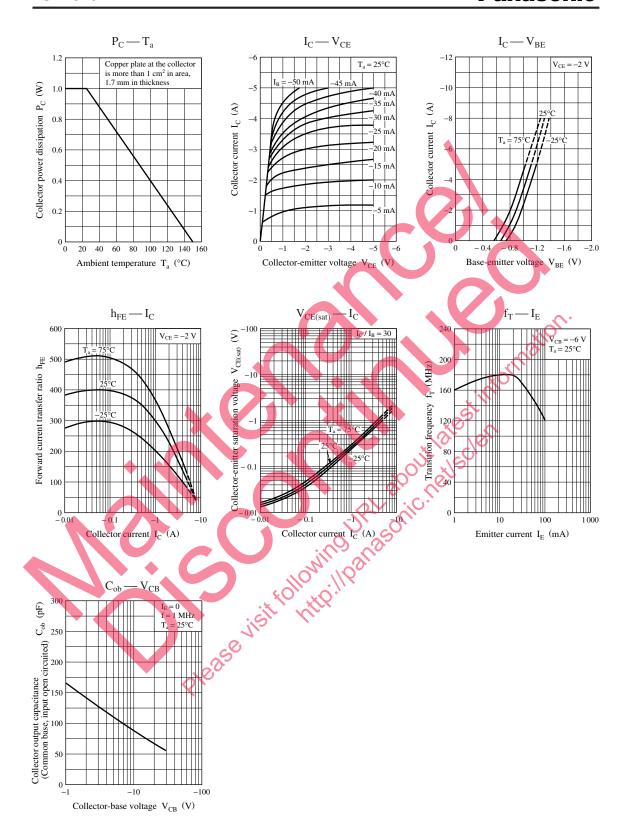
	-Cr			~ ( ) ·	$M\Delta$	1 Package			
Collector power dissipation *	or power dissipation * Pc 1 W								
Junction temperature T <sub>i</sub> 150 °C									
Junction temperature  T <sub>j</sub> 150  Storage temperature  T <sub>stg</sub> -55 to +150  C  Note) *: Print circuit board: Copper foil area of 1 cm <sup>2</sup> or more, and the board thickness of 1.7 mm for the collector portion  Flectrical Characteristics T <sub>stg</sub> = 25°C + 3°C									
Note) *: Print circuit board: Copper foil area of 1 cm <sup>2</sup> or more, and the board									
thickness of 1.7 mm for the collector portion									
300,000									
■ Electrical Characteristics T <sub>a</sub> = 25°C ± 3°C									
Electrical Characteristics T <sub>a</sub>	= 23 C	-30°							
Parameter	Symbol	Conditions	Min	Тур	Max	Unit			
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = -1 \text{ mA} \cdot I_B = 0$	-20			V			
Emitter-base voltage (Collector open)	$V_{\rm EBO}$	$I_E = 10  \mu A, I_C = 0$	-7			V			
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = -10 V_{c} I_{E} = 0$			-100	nA			
Emitter-base cutoff current (Collector open)	I <sub>EBO</sub>	$V_{EB} = -5 \text{ V}, I_{C} = 0$			-1	μΑ			
Forward current transfer ratio *1, 2	$h_{\rm FE}$	$V_{CE} = -2 \text{ V}, I_{C} = -2 \text{ A}$	90		625	_			
Collector-emitter saturation voltage *1	V <sub>CE(sat)</sub>	$I_C = -3 \text{ A}, I_B = -0.1 \text{ A}$			-1	V			
Transition frequency	$f_T$	$V_{CB} = -6 \text{ V}, I_E = 50 \text{ mA}, f = 200 \text{ MHz}$		120		MHz			
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -20 \text{ V}, I_E = 0, f = 1 \text{ MHz}$			85	pF			
(Common base, input open circuited)									

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

#### 2. \*1: Pulse measurement

#### \*2: Rank classification

Rank	Р	Q	R
$h_{FE}$	90 to 135	120 to 205	180 to 625



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